



DS-03-007

October 15, 2003

To: Commissioner for Patents  
P.O.Box 1450  
Alexandria, VA 22313-1450

Fr: George O. Saile, Reg. No. 19,572  
28 Davis Avenue  
Poughkeepsie, N.Y. 12603

Subject:

Serial No. 10/618,535 07/11/03

Andreas Sibrai et al.

ENHANCED ARCHITECTURES OF VOLTAGE-  
CONTROLLED OSCILLATORS WITH SINGLE  
INDUCTOR (VCO-1L)

Grp. Art Unit:

#### INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation  
In An Application.

The following Patents and/or Publications are submitted to  
comply with the duty of disclosure under CFR 1.97-1.99 and  
37 CFR 1.56. Copies of each document is included herewith.

#### CERTIFICATE OF MAILING

I hereby certify that this correspondence is being  
deposited with the United States Postal Service as first class  
mail in an envelope addressed to: Commissioner for Patents,  
P.O. Box 1450, Alexandria, VA 22313-1450, on October 20, 2003.

Stephen B. Ackerman, Reg.# 37761

Signature/Date

 10/20/03

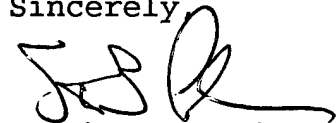
The initial topology of a differential one-inductor VCO (VCO-1L) is discussed in the publication of Jan Craninckx et al., "A Full Integrated CMOS DCS-1800 Frequency Synthesizer," IEEE Journal of Solid-State Circuits, Vol. 33, No. 12, Dec. 1998, pp. 2054-2065.

U.S. Patent 6,486,744 to Cann, "Low Phase Noise Voltage-Controlled Oscillator and Method of Using the Same," discloses a low phase noise voltage-controlled oscillator (VCO) and method.

U.S. Patent 6,353,368 to Iravani, "VCO Circuit Using Negative Feedback to Reduce Phase Noise," discloses a low phase noise CMOS voltage controlled oscillator (VCO) circuit.

U.S. Patent 6,181,216 to Waight, "Low Phase-Noise Voltage Controlled Oscillator," discloses an oscillator using a Field-Effect Transistor (FET) in a Colpitts configuration.

Sincerely

A handwritten signature in black ink, appearing to read 'SBA', with a long horizontal flourish extending to the right.

Stephen B. Ackerman,  
Reg. No. 37761

Agitation Number

10/618,535

Andreas Sibrai et al.

07/11/03

Orcaus Art Unit

01 INFORMATION DISCLOSURE CITATION  
IN AN APPLICATION

OCT 22 2003

(Use several sheets if necessary)

## U. S. PATENT DOCUMENTS

O I R E O R M  
 OCT 22 2003  
 PATENT & TRADEMARK OFFICE  
 RECEIVED

## FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

"A Full Integrated cmos DCS-1800 Frequency Synthesizer", by Jan Craninckx et al., IEEE Journal of Solid-State Circuits, Vol. 33, No. 12, Dec. 1998, pp. 2054-2065.

DATE CONFIRMED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant